Oocket No.: 42390P13502

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of:

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PAUL E LUSE, ET AL.

Application No.: 10/090,944

Filed: March 4, 2002

For: **CONTROL PROCEDURE SELECTION** 

Art Group: 2818

Examiner: Not yet assigned

#### INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

TAYLOR & ZAFMAN LLP

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Date: August 20, 2004

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of

Complete if Known				
Application Number	10/090,944	· · · · · · · · · · · · · · · · · · ·		
Filing Date	March 4, 2002			
First Named Inventor	Paul E Luse			
Art Unit	2818			
Examiner Name	Not yet assigned			
Attorney Docket Number	42390P13502			

	U.S. PATENT DOCUMENTS					
		Document Number	Publication Date	Name of Patralia	Pages, Columns, Lines, Where	
Examiner Initials*	Cite No.1	Number - Kind Code² (if known)	or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Relevant Passages or Relevant Figures Appear	
	1	US-6,260,094 B1	07-10-2001	Davis et al.		
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	FOREIGN PATENT DOCUMENTS							
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#### INFORMATION DISCLOSURE STATEMENT BY APPLICANT

of

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Complete if Known Application Number 10/090,944 Filing Date March 4, 2002 First Named Inventor Paul E Luse Art Unit 2818 Examiner Name Not yet assigned Attorney Docket Number 42390P13502

NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial Examiner Cite symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. Initials\* R. Russell, BIOS Basics Online!, June 5, 2001, one page 2 A. Tischenko, "Zero-Channel RAID. The Economic Approach", Computer Review Online!, 3 four pages Intel Corporation, "DK440LX Motherboard - Technical Product Specification", October 4 1997, 106 pages Intel Corporation, "MROMB Design Considerations Using the Intel 80303 I/O Processor -Application Note" September 2001, 15 pages Copy of PCT Search Report dated April 19, 2004 6

Examiner	Date	
Signature	Cons	sidered

'Applicant's unique citation designation number. Applicant is to place a check mark here if English language Translation is attached.

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Application Number
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First Named Inventor
Art Unit
Examiner Name
Attorney Docket Number

Complete if Known

10/090, 944

3.4.02

Paul Luse

Sheet 3 of 4 Attorney Docket Number 42P18502

	NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²	
	1	Adaptec - Adaptec SCSI RAID 2000S Support		
	2	Adaptec - Adaptec SCSI RAID 2005S Support	٢	
	3	Adaptec - Motherboard Partners and Compatibility		
	4	Adaptec - Adaptec SCSI RAID 2005S Technical SPecs		
	5	Adpatec - Adaptec Article: Zero Channel RAID Frequently Asked Questions		
	6	Intel Corp., Product Brief, Intel Server Board SCB2	-	
	7	Serial ATA: High Speed Serialized AT Attachment Revison 1.0, 29 August 2001		
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	9 .	PCI Local Bus, PCI BIOS Specification, Revision 2.1 August 26, 1994		
	10	PCI Local Bus, PCT-to-PCI Bridge Architecture Specification, Revison 1.1, December 16, 1998		
	11	PCI Local Bus, PCI Local Bus Specification, Revision 2.2, December 18, 1998		
	12	PCI Local Bus, Mini PCI Specification, Revision 1.0, October 25, 1999		
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	14	PCI Local Bus, PCI Local Bus Specification, Revision 2.3, March 29, 2002	$\vdash$	

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Signature	Considered	4	

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Examiner Name		
Attorney Docket Number	42.012002	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	aminer Cite Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T²
	15	PCI Local Bus, Appendix D, Class Codes	
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